

UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S) RAUBUCH, MARTIN GROUP ART UNIT: 2112  
APPLN. NO.: 10/531756 EXAMINER: William B. Partridge  
FILED: 10/6/2003  
TITLE: ARRANGEMENT, SYSTEM AND METHOD FOR VECTOR  
PERMUTATION IN SINGLE-INSTRUCTION MULTIPLE-DATA  
MICROPROCESSORS

---

**Certificate of Submission**

I hereby certify that this correspondence is being submitted to the USPTO, Alexandria, VA.

- ☐ Addressed per C.F.R. § 1.1(a) and deposited with the United States Postal Service with sufficient postage as first class mail.
- ☐ Facsimile transmitted in accordance with C.F.R. § 1.6(d).
- ☒ Submitted electronically via EFS in accordance with "Legal Framework for EFS Web".

5-17-07

\_\_\_\_\_  
Date of Submission

\_\_\_\_\_  
/Stacie Herrera/  
Signature

\_\_\_\_\_  
Stacie Herrera  
Printed Name of Person Signing Certificate

**AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action dated April 12, 2007, and Examiner's comments with regard thereto, please enter the following Amendment in the above-entitled application, without prejudice or disclaimer.

A Petition for Extension of Time to respond, with fee authorization, is submitted concurrently herewith.

If Applicant has overlooked any additional fees, or if any overpayment has been made, the Commissioner is hereby authorized to credit or debit Deposit Account 503079, Freescale Semiconductor, Inc.